IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	J
Filing Date)
Inventor	,
Assignee Micron Technology, Inc.	
Group Art Unit	ļ
Examiner	•
Attorney's Docket No	j
Title: Semiconductor Processor Systems, A System Configured to Provide a	l
Semiconductor Workpiece Process Fluid	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Reg. No. 39,833

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Sheet I of I .

Form PTO-1449 U.S. DEPARTMENT OF COMMER PATENT AND TRADEMARK OFFI LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				ADEMARK OFFICE	ATTY. DOCKET NO. SERIAL NO. 08/517,127 APPLICANT				
						Moore et al.			
					FILING DATE March 12, 2000			GROUP 3724	
			Ų.S.	PATENT DOCUMEN	TS				
*Examiner Initial		Document Number	Date	Name		Class	Subclass	Filing I If Appro	
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	AE								
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		OTHER F	REFERENCES (Inc	cluding Author, Title, [Date, Pertin	ent Pages, E	Etc.)		
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*EXAMINER	: Initiat i	f reference considered. In	ered, whether or n	ot citation is in confor form with next commu	mance with	MPEP 609; applicant.	Draw line thro	ugh citation	if not

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